	Application No.	Applicant(s)
Notice of Allowability	10/736,672	KITABATAKE ET AL.
	Examiner	Art Unit
	Allan R. Wilson	2815
The MAILING DATE of this communication appear All claims being allowable, PROSECUTION ON THE MERITS IS (herewith (or previously mailed), a Notice of Allowance (PTOL-85) of NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RICE of the Office or upon petition by the applicant. See 37 CFR 1.313	OR REMAINS) CLOSED in or other appropriate commu- GHTS. This application is si	this application. If not included nication will be mailed in due course. THIS
1. This communication is responsive to the election filed on 07	<u>'/25/2005</u> .	
2. The allowed claim(s) is/are <u>1-6</u> .		
3.	been received. been received in Applicatior	n No
International Bureau (PCT Rule 17.2(a)). * Certified copies not received:		•
Applicant has THREE MONTHS FROM THE "MAILING DATE" or noted below. Failure to timely comply will result in ABANDONME THIS THREE-MONTH PERIOD IS NOT EXTENDABLE. 4. A SUBSTITUTE OATH OR DECLARATION must be submitted in the submi	ENT of this application. ted. Note the attached EXA	MINER'S AMENDMENT or NOTICE OF
	`, `	designation to designate.
 CORRECTED DRAWINGS (as "replacement sheets") must (a) including changes required by the Notice of Draftsperso 		/ PTO 948) attached
1) hereto or 2) to Paper No./Mail Date	1131 atent Diawing Neview	(FTO-940) attached
(b) ☐ including changes required by the attached Examiner's Paper No./Mail Date	Amendment / Comment or i	in the Office action of
Identifying indicia such as the application number (see 37 CFR 1.8 each sheet. Replacement sheet(s) should be labeled as such in the	4(c)) should be written on the header according to 37 CFF	e drawings in the front (not the back) of R 1.121(d).
6. DEPOSIT OF and/or INFORMATION about the deposi attached Examiner's comment regarding REQUIREMENT FOR	it of BIOLOGICAL MATE OR THE DEPOSIT OF BIO	RIAL must be submitted. Note the LOGICAL MATERIAL.
Attachment(s)	_	
1. Notice of References Cited (PTO-892)		ormal Patent Application (PTO-152)
2. Notice of Draftperson's Patent Drawing Review (PTO-948)	6. ∐ Interview Sur Paper No./N	mmary (PTO-413), ⁄Iail Date
 Information Disclosure Statements (PTO-1449 or PTO/SB/08) Paper No./Mail Date), 7. ⊠ Examiner's A	Amendment/Comment
Examiner's Comment Regarding Requirement for Deposit of Biological Material	8. 🛛 Examiner's S	Statement of Reasons for Allowance
	9.	

DETAILED ACTION

Election/Restrictions

Applicant's election of Species I, claims 1-6, in the reply filed on July 25, 2005 is acknowledged. Because applicant did not distinctly and specifically point out the supposed errors in the restriction requirement, the election has been treated as an election without traverse (MPEP § 818.03(a)).

EXAMINER'S AMENDMENT

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

The application has been amended as follows:

Replace the present title with: --Stacked Semiconductor Transistors--.

This application is in condition for allowance except for the presence of claims 7-9 to an invention non-elected without traverse. Accordingly, claims 7-9 have been cancelled.

REASONS for ALLOWANCE

The following is an examiner's statement of reasons for allowance:

A close prior art, U.S. Patent No. 5,793,115 to Zavracky et al., discloses most of the limitations of the claims, but fails to show at least a second electrode formed in a back surface

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side of the first substrate and a forth electrode formed in a back surface side of the second substrate.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure: Wenzel et al. (illustrates two chips connected together), Walker et al. (illustrates two transistors connected together in FIG. 11) and Japanese Patent Application No. 59-107554 (illustrates plural SiC with Si layer laminated together).

Any inquiry concerning this communication or earlier communications from an examiner should be directed to Primary Examiner Allan Wilson whose telephone number is (571) 272-1738. Examiner Wilson can normally be reached 7:00-4:00 Monday-Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tom Thomas can be reached on (571) 272-1664. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Allan R. Wilson Primary Examiner 31 August 2005